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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re patent application of:  
Park, et al.

Serial No.: 10/604,912

Filed: August 26, 2003


Group Art Unit: 2823

Examiner: Kebede, Brook

Atty. Docket No.: FIS920030026US1

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571-273-8300) on 8/15/06



Pamela M. Riley

For: METHOD TO PRODUCE TRANSISTOR HAVING REDUCED GATE  
HEIGHT

**AMENDMENT UNDER 37 C.F.R. §1.116**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This amendment is in response to the Office Action mailed July 5, 2006, setting a  
three-month statutory period for response. Therefore, this amendment is timely filed.

Please amend the above-identified patent application as follows:

10/604,912

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Central fax received 17 pages  
(missing 11 pages)

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